

Performance Test(Wire Speed) with Factory Image

Environment: Modified "ccip_std_afu.sv"

```

assign egr_out_sop[0] = ing_in_sop[0];
assign egr_out_eop[0] = ing_in_eop[0];
assign egr_out_val[0] = ing_in_val[0];
assign egr_out_mty[0] = ing_in_mty[0];
assign egr_out_dat[0] = ing_in_dat[0];
assign egr_out_err[0] = ing_in_err[0];
assign egr_out_cha[0] = ing_in_cha[0];
assign ing_in_rdy[0] = egr_out_rdy[0];

assign ing_out_sop[0] = 1'b0;
assign ing_out_eop[0] = 1'b0;
assign ing_out_val[0] = 1'b0;
assign ing_out_mty[0] = 6'b0;
assign ing_out_dat[0] = 512'b0;
assign ing_out_err[0] = 1'b0;
assign ing_out_cha[0] = 3'b0;

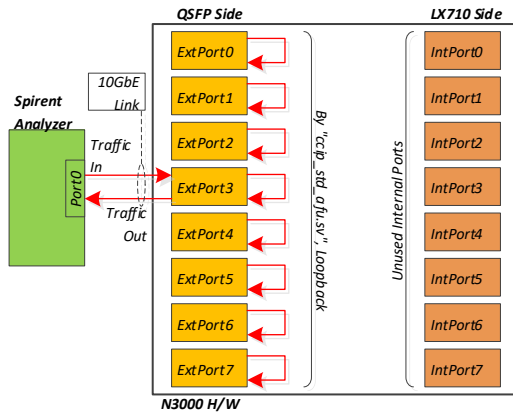
assign egr_in_rdy[0] = 1'b1;

assign ing_in_fpga_internal_pause_req[0] = 8'b0;
assign egr_in_fpga_internal_pause_req[0] = 8'b0;
    
```

✘ Unused Pause function by all req signal is low

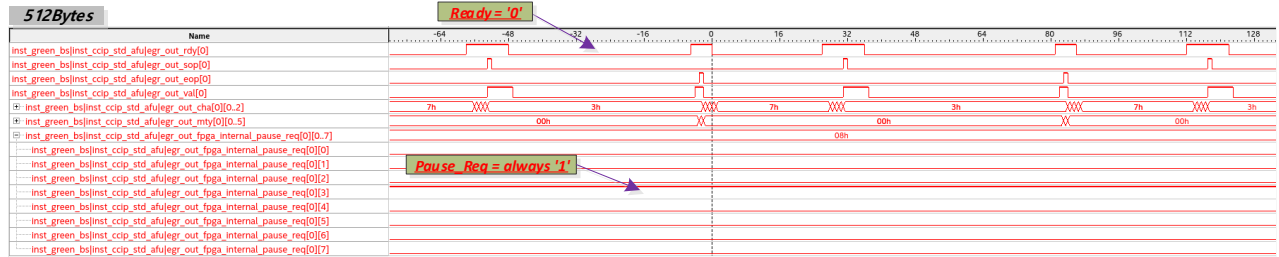
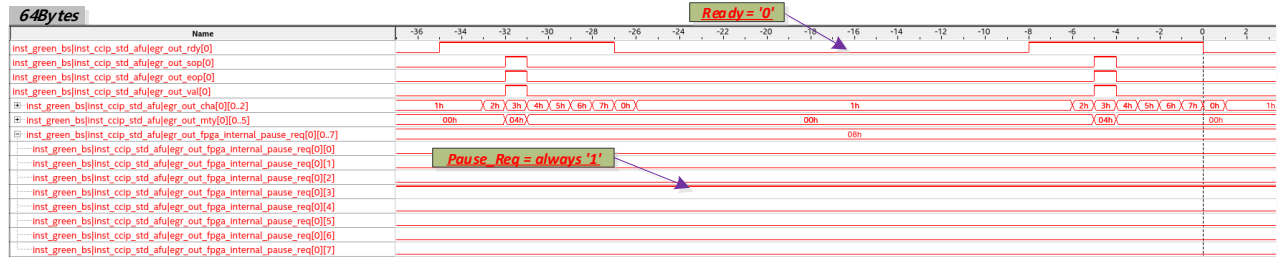
Environment: Performance Test

Connection



Spirent Analyzer Setup

Traffic 100%@10GbE to ExtPort3 : Result = Packet loss occurs



Traffic 99.99%@10GbE to ExtPort3: Result = No packet loss

